

6.5.2.4 Tile pre-processor

K. De, G. Usai University of Texas at Arlington

University of Valencia Spain

University of Witwatersrand South Africa





Fund	WBS	Tag	Description	AY k\$	FY17	FY18	FY19	FY20	FY21	FY22	FY23	FY24	FY25	То	tal
NSF	6.5.2.4	PPR	Preprocessor	Total		•	- 63.05	183.13	196.53	199.32	46.38		•	-	688.4
				Labo	or	-	- 54.25	103.25	92.87	95.66	37.58		-	-	383.6
				Materia	al	-		71.08	94.86	94.86	-		-	-	260.80
				Trave	el	-	- 8.80	8.80	8.80	8.80	8.80		-	-	44.00
				CORE		-		21.08	94.86	94.86	-		-	-	210.80
				FTEs		-	- 0.37	0.91	1.51	1.51	0.23		-	-	4.5
		PPR2010	Final Design	Total											
				Labo	or		54.25	-			-		-	-	54.2
				Materia	al										
				Trave			8.80								8.8
				CORE		-					-		-	-	
				FTEs		-	- 0.37	_			-		-	-	0.3
		PPR2020	Parts Procurement/Q&A	Total											
				Labo	or		<u> </u>	5.16			-		-	_	5.1
				Materia				21.08							21.0
				Trave				21.00							21.0
				CORE		_		21.08			_		_	_	21.0
				FTEs		_		0.03					_		0.0
		PPR2030	Burn-in/Test/repair	Total				0.03					_		0.0
		11112030	Bull-liv lestrepail	Labo				82.15							82.1
				Materia				02.13		_			_	_	02.1
				Trave				8.80							8.8
				CORE	žI		_	0.00							0.0
				FTEs		-	-	0.72	-	-	-		-	-	0.7
		DDD0040	Total Foundation and foliable and			-	-	0.73		-	-		-	-	0.7
		PPR2040	Test Equipment fabrication	Total	_			45.04							45.0
				Labo	_			15.94		-	-		-	-	15.9
				Materia				50.00							50.0
				Trave	el										
				CORE											
				FTEs		-	-	0.14	-	-	-		-	-	0.1
		PPR2050	Parts Procurement/Q&A	Total											
				Labo				-	4.43		-		-	-	8.9
				Materia					94.86	94.86					189.7
				Trave	el										
				CORE		-	-	-	94.86		-		-	-	189.7
				FTEs		-		-	0.03	0.03	-		-	-	0.0
		PPR2060	Burn-in/Test/repair	Total											
				Labo				-	88.44	91.10	37.58		-	-	217.1
				Materia	al										
				Trave	el				8.80	8.80	8.80				26.4
				CORE		-	-	-	-	-	-		-	-	
				FTEs		-		_	1.48	1.48	0.23		-	-	3.18

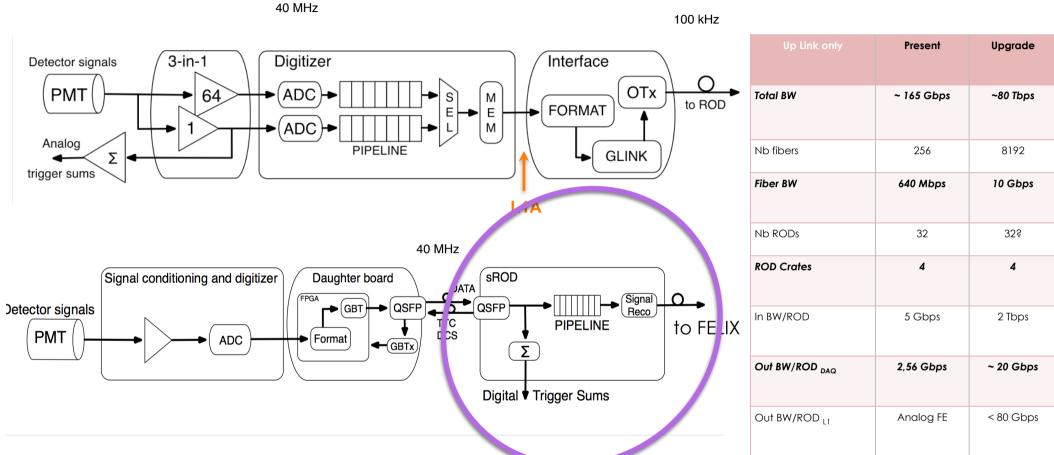
G. Usai -UTArlington 22-Oct-2015

6.5.2.4 Tile pre-processor

			Production cost fo	or the sROD systel	Moni Scobi	ng aocum	IEIII
Item	number/unit	item cost	unit cost in EURO				
ATCA ovotom	4	15000		C0000	4	60000	
ATCA system Chassis - Shelf	4	15000		60000	4	60000	
Shelf Manager							
Power Supply							
sROD blades	32	22300		713600	40	892000	
Virtex 7	4	2000	8000				
Kintex 7	1	1000	1000				
QSFPs	32	250	8000				
Other components	1	3000	3000				
PCB cost	1	800	800				
Assembly	1	1500	1500				
sROD Transition Module	32	5050		161600	40	202000	
				101000			
Kintex 7	1	1000	1000				
	1 4	1000 250	1000 1000				
MiniPOD Tx-connector L0/L1		250	1000 1000 750				
MiniPOD Tx-connector L0/L1 QSFP Felix2	4	250	1000				
MiniPOD Tx-connector L0/L1 QSFP Felix2 Other components	4 3	250 250	1000 750				
MiniPOD Tx-connector L0/L1 QSFP Felix2 Other components PCB production	4 3 1	250 250 1000	1000 750 1000				
MiniPOD Tx-connector L0/L1 QSFP Felix2 Other components PCB production PCB assembly	4 3 1 1 1	250 250 1000 500 800	1000 750 1000 500 800				
MiniPOD Tx-connector L0/L1 QSFP Felix2 Other components PCB production PCB assembly	4 3 1 1 1	250 250 1000 500 800	1000 750 1000 500 800	40000	4	40000	
MiniPOD Tx-connector L0/L1 QSFP Felix2 Other components PCB production PCB assembly	4 3 1 1 1	250 250 1000 500 800	1000 750 1000 500 800		4		
MiniPOD Tx-connector L0/L1 QSFP Felix2 Other components PCB production PCB assembly	4 3 1 1 1	250 250 1000 500 800	1000 750 1000 500 800	40000 975200 1170240	4	1194000	EURO
MiniPOD Tx-connector L0/L1 QSFP Felix2 Other components PCB production PCB assembly Additional TTC, DCS modules per pa	4 3 1 1 1	250 250 1000 500 800	1000 750 1000 500 800	975200	4		EURO
MiniPOD Tx-connector L0/L1 QSFP Felix2 Other components PCB production PCB assembly Additional TTC, DCS modules per pa	4 3 1 1 1 1	250 250 1000 500 800	1000 750 1000 500 800	975200	4	1194000	EURO
Kintex 7 MiniPOD Tx-connector L0/L1 QSFP Felix2 Other components PCB production PCB assembly Additional TTC, DCS modules per pa	4 3 1 1 1 1	250 250 1000 500 800	1000 750 1000 500 800	975200	4	1194000	EURO
MiniPOD Tx-connector L0/L1 QSFP Felix2 Other components PCB production PCB assembly Additional TTC, DCS modules per pa	4 3 1 1 1 1	250 250 1000 500 800	1000 750 1000 500 800	975200	4	1194000	EURO

Tile readout changes

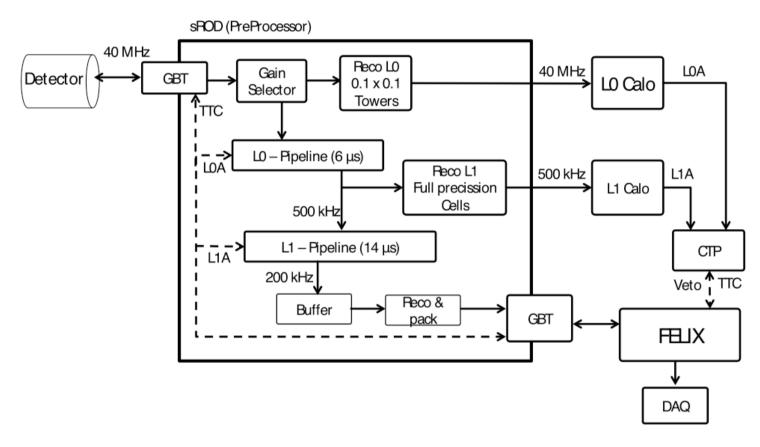




- Complete replacement of on-detector and off-detector electronics
- remove trigger limitation: higher L0/L1 trigger rate, larger latency
- digital trigger information with full granularity and better precision
 - Pipelines and DCS & TTC interfaces moved off-detector

Tile pre-processor

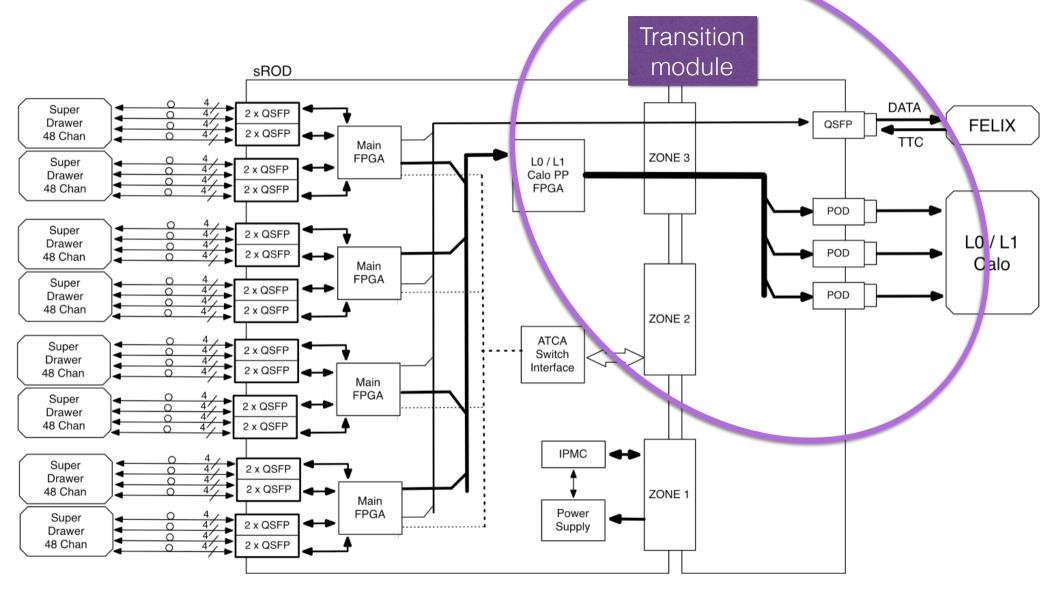




- interface the Front End electronics with the TTC and DCS system
- receive the data @ 40 MHz store in pipelines during L0 and L1 trigger decision
- Signal reconstruction for each channels and data reduction (trigger primitive formation) to input the L0/L1 Calo trigger.

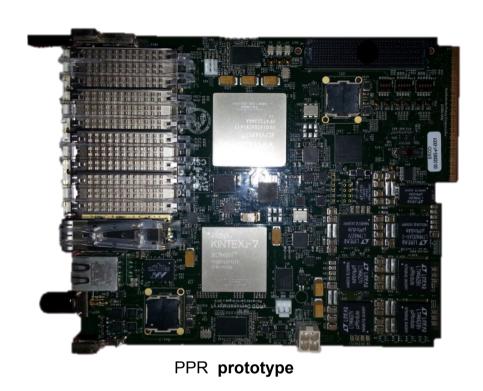
Possible layout for the Tile PPR





PPR prototype for the demonstrator





TTC

commercial emulator vc707+QSFP-FMC

- first prototype designed and constructed (mid size AMC:180.6 mm x 148.5 mm)
- now fully functional
- development of firmware is well advanced using the VC707 emulator.
- now firmware migration and integration in the ATCA end of the year

A portable test-bench for validation of phase-II electronics

- A portable readout module for minidrawers (Prometeo) is under development
 - Stand-alone test-bench to assess the QA of the electronics



- Based on a Virtex 7 evaluation board
- QSFP module provides optical connection
- HV and LED driver boards test response of **PMTs**
- 16 channel ADC mezzanine to digitize the output of trigger cables from previous testbench

Software

- Based on IPbus, QT framework
- Modular implementation to allow particular test implementation

Status

- All hardware components in hand,
- Firmware under design







QSFP FMC module



HV PS



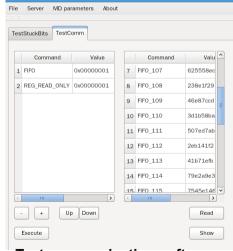
LED drive



16 channel ADC (hybrid demonstrator only)



System power supply (commercial ATX + 24V)



Test communication software panel